

Notice of Allowability	Application No.	Applicant(s)	
	10/728,241	WEST ET AL.	
	Examiner	Art Unit	
	Nitin Patel	2673	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 06/21/2005.
2. ☒ The allowed claim(s) is/are 1-26.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| <ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date <u>12/03/2003</u> 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____. |
|--|--|

REASON FOR ALLOWANCE

Terminal Disclaimer

1. The terminal disclaimer filed on 06/14/2005 disclaiming the terminal portion of any patent granted on this application, which would extend beyond the expiration date of 04/04/2001 has been reviewed and is accepted. The terminal disclaimer has been recorded.
2. Claims 1-26 have been allowed.

The following is an examiner's statement of reason for allowance:

The prior art fails to teach or suggest a display system adapted to display input image data received one frame at a time at an input frame rate and an input resolution on a display having an output frame rate and an output resolution having a pixel packing circuit adapted to generate coded image data responsive to a failsafe enable signal, the coded image data being a compressed representation of the input image data including a plurality of pixel bits and a palette bit, the palette bit to select one of a plurality of levels in a color palette for representing each of the pixel bits and a pixel unpacking circuit adapted to generate output image data capable of being displayed on the display by manipulating the coded image data responsive to the palette bit as claimed in claim

1.

The prior art fails to teach or suggest an input image signals provided to the system one frame at a time, the input image signals having input characteristics; a display capable of displaying output image signals having output characteristics failsafe enable adapted to identify when the input characteristics exceeds the output

Art Unit: 2673

characteristics, a packing circuit adapted to generate a coded signal by compressing the input image signals responsive to the failsafe enable, the coded signal including a plurality of pixel bits and a palette bit, the palette bit being adapted to select one of a plurality of levels in a color palette for representing each of the pixel bits; a memory adapted to store the coded signal and an unpacking circuit adapted to generate the output image signals at the output characteristics by logically manipulating the coded signal responsive to the palette bit as claimed in claim 11.

The prior art fails to teach or suggest a failsafe circuit having an input pixel formatter circuit adapted to receive input signal at an input frame rate and an input resolution representative of an image and adapted to generate a coded signal representing a compressed version of the image, the coded signal including a plurality of pixel bits and a palette bit to select one of plurality of levels in a palette; a memory buffer adapted to store the coded signal and a pixel value generator circuit adapted to generate output signals by manipulating the coded signals responsive to the palette bit as claimed in claim 16.

The prior art fails to teach or suggest a method for automatically displaying digital image data having an input frame rate on a display having an output frame rate having enabling a failsafe signal if the input frame rate exceeds the output frame rate; compressing the image data responsive to the failsafe signal, the compressed image data including a palette bit to select one of a plurality of levels in a palette; displaying the compressed image data and allowing a user to change display setting after displaying the compressed image data as claimed in claim 23.

Art Unit: 2673

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

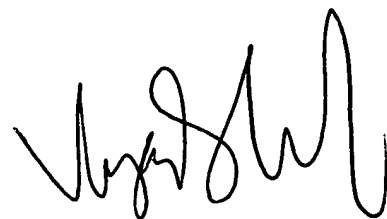
4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NP

August 31, 2005

A handwritten signature in black ink, appearing to read 'Vijay Shankar', with a stylized, cursive script.

**VIJAY SHANKAR
PRIMARY EXAMINER**